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**Inventor Name Search Result**

Your Search was:

Last Name = GROEN

First Name = ERIC

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09152869</a>	<a href="#">6121800</a>	150	09/14/1998	IMPEDANCE MATCHED, VOLTAGE-MODE H-BRIDGE WRITE DRIVERS	GROEN, ERIC
<a href="#">09630494</a>	<a href="#">6285221</a>	150	08/02/2000	IMPEDANCE-MATCHED, VOLTAGE-MODE H-BRIDGE WRITE DRIVERS	GROEN, ERIC
<a href="#">09706904</a>	<a href="#">6437599</a>	150	11/06/2000	PROGRAMMABLE LINE DRIVER	GROEN, ERIC
<a href="#">11114807</a>	<a href="#">7167410</a>	150	04/26/2005	MEMORY SYSTEM AND MEMORY DEVICE HAVING A SERIAL INTERFACE	GROEN, ERIC
<a href="#">11178958</a>	Not Issued	30	07/11/2005	Method of encoding and synchronizing a serial interface	GROEN, ERIC
<a href="#">10097689</a>	<a href="#">6683502</a>	150	03/12/2002	PROCESS COMPENSATED PHASE LOCKED LOOP	GROEN, ERIC D.
<a href="#">10407281</a>	<a href="#">6882224</a>	150	04/03/2003	SELF-BIASING FOR COMMON GATE AMPLIFIER	GROEN, ERIC D.
<a href="#">10659803</a>	Not Issued	30	09/11/2003	Analog front-end having built-in equalization and applications thereof	GROEN, ERIC D.
<a href="#">10659819</a>	<a href="#">7109809</a>	150	09/11/2003	METHOD AND CIRCUIT FOR REDUCING VCO NOISE	GROEN, ERIC D.
<a href="#">10659916</a>	<a href="#">7047457</a>	150	09/11/2003	TESTING OF A MULTI-GIGABIT TRANSCEIVER	GROEN, ERIC D.
<a href="#">10659971</a>	Not Issued	93	09/11/2003	RECEIVER TERMINATION NETWORK AND APPLICATION THEREOF	GROEN, ERIC D.
<a href="#">10659973</a>	<a href="#">7015838</a>	150	09/11/2003	PROGRAMMABLE SERIALIZING DATA PATH	GROEN, ERIC D.
<a href="#">10659978</a>	<a href="#">6956442</a>	150	09/11/2003	RING OSCILLATOR WITH PEAKING STAGES	GROEN, ERIC D.
<a href="#">10660062</a>	<a href="#">6975132</a>	150	09/11/2003	DAC BASED DRIVER WITH	GROEN, ERIC D.

				SELECTABLE PRE-EMPHASIS SIGNAL LEVELS	
<a href="#">10660159</a>	<a href="#">6976102</a>	150	09/11/2003	INTEGRATED CIRCUIT WITH AUTO NEGOTIATION	GROEN, ERIC D.
<a href="#">10660190</a>	Not Issued	80	09/11/2003	Programmable on chip regulators with bypass	GROEN, ERIC D.
<a href="#">10660254</a>	Not Issued	71	09/11/2003	MGT/FPGA clock management system	GROEN, ERIC D.
<a href="#">10660448</a>	<a href="#">6870390</a>	150	09/11/2003	TX LINE DRIVER WITH COMMON MODE IDLE STATE AND SELECTABLE SLEW RATES	GROEN, ERIC D.
<a href="#">10661016</a>	Not Issued	71	09/11/2003	Programmable logic device including programmable multi- gigabit transceivers	GROEN, ERIC D.
<a href="#">10812549</a>	<a href="#">7196545</a>	150	03/29/2004	HIGH FREQUENCY LATCH	GROEN, ERIC D.
<a href="#">10990047</a>	<a href="#">7142014</a>	150	11/16/2004	HIGH FREQUENCY XOR WITH PEAKED LOAD STAGE	GROEN, ERIC D.
<a href="#">11218962</a>	<a href="#">7227375</a>	150	09/01/2005	DAC BASED DRIVER WITH SELECTABLE PRE-EMPHASIS SIGNAL LEVELS	GROEN, ERIC D.
<a href="#">11326548</a>	<a href="#">7116251</a>	150	01/04/2006	PROGRAMMABLE SERIALIZING DATA PATH	GROEN, ERIC D.
<a href="#">09968471</a>	<a href="#">6507220</a>	150	09/28/2001	CORRECTION OF DUTY- CYCLE DISTORTION IN COMMUNICATIONS AND OTHER CIRCUITS	GROEN, ERIC DOUGLAS
<a href="#">10294254</a>	<a href="#">6690202</a>	150	11/13/2002	CORRECTION OF DUTY- CYCLE DISTORTION IN COMMUNICATIONS AND OTHER CIRCUITS	GROEN, ERIC DOUGLAS

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